



U30 ATMEGA128-MLF64

1	/PEN	AVCC
2	RXD0 (PDI) PE0	GND
3	TXD0 PDI PE1	GND
4	XCK0 AN0 PE2	AREF
5	OC3A AN1 PE3	ADC1
6	OC3B INT4 PE4	ADC2
7	OC3C INT5 PE5	ADC3
8	T3 INT6 PE6	ADC4
9	IC3 INT7 PE7	ADC5
10	/SS PB0	PF4 ADC4 TCK
11	/SCK PB1	PF5 ADC5 TMS
12	MOSI PB2	PF6 ADC6 TDI
13	MISO PB3	PF7 ADC7 TDO
14	SCK PB4	GND
15	OC1A PB5	VCC
16	OC1B PB6	PA0 ADD
17	OC2 OC1C PB7	PA1 AD1
18	TOSC2 PG3	PA2 AD2
19	TOSC1 PG4	PA3 AD3
20	/RESET	PA4 AD4
21	VCC	PA5 AD5
22	GND	PA6 AD6
23	XTAL2	PA7 AD7
24	XTAL1	PG2 ALE
25	SCL INT0 PD0	PC7 A15
26	SDA INT1 PD1	PC6 A14
27	RXD1 INT2 PD2	PC5 A13
28	TXD1 INT3 PD3	PC4 A12
29	IC1 PD4	PC3 A11
30	XCK1 PD5	PC2 A10
31	T1 PD6	PC1 A9
32	T2 PD7	PC0 A8
	EPAD	PG1 /RD
	EPAD	PG0 /WR
	EPAD	
	EPAD	
	EPAD	
	EPAD	

<p>chip45 chip45 GmbH & Co. KG http://www.chip45.com</p>	Project	Very Small SPS with AVR MCU		
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